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FIG. 4

3-BIT PRC → 1110100011101
CYCLICAL EXPANSION

TABLE OF ASSOCIATION

POSITION	ADDRESS
FIRST POSITION :	111010
SECOND POSITION :	110100
THIRD POSITION :	101000
FOURTH POSITION :	010001
FIFTH POSITION :	100011
SIXTH POSITION :	000111
SEVENTH POSITION :	001110
EIGHTH POSITION :	011101

FIG. 5a

POSITION : 111010 110100 101000 010001 100011 000111 001110 011101
SCANNING : 110100 110100 110100 110100 110100 110100 110100 110100
COMPARISON: XX__X XXXXXX X__XX _XX_X_ X_X__ _XX_ _X_X _X_XX_
RESULT : ONE MATCH FOR A PREDETERMINED SECOND POSITION → POSITION VALID

FIG. 5b

POSITION : 111010 110100 101000 010001 100011 000111 001110 011101
SCANNING : F10100 F10100 F10100 F10100 F10100 F10100 F10100 F10100
COMPARISON: XX__X XXXXXX X__XX XXX_X_ X_X__ X_XX_ X__X_X XX_XX_
RESULT : ONE MATCH FOR A PREDETERMINED SECOND POSITION → POSITION VALID

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FIG. 5c

POSITION : 111010 110100 101000 010001 100011 000111 001110 011101
 SCANNING : FIOFOO FIOFOO FIOFOO FIOFOO FIOFOO FIOFOO FIOFOO FIOFOO
 COMPARISON: XX_X_X XXXXXX X_XXX XXXXX_ X_XX_ X_XX_ X_X_X XX_XX_
 RESULT : ONE MATCH FOR A PREDETERMINED SECOND POSITION → POSITION VALID

FIG. 5d

POSITION : 111010 110100 101000 010001 100011 000111 001110 011101
 SCANNING : FIOFFO FIOFFO FIOFFO FIOFFO FIOFFO FIOFFO FIOFFO FIOFFO
 COMPARISON: XX_XXX XXXXXX X_XXX XXXXX_ X_XXX_ X_XXX_ X_XXX_ XX_XX_
 RESULT : ONE MATCH FOR A PREDETERMINED SECOND POSITION → POSITION VALID

FIG. 5e

POSITION : 111010 110100 101000 010001 100011 000111 001110 011101
 SCANNING : FIOFOF FIOFOF FIOFOF FIOFOF FIOFOF FIOFOF FIOFOF FIOFOF
 COMPARISON: XX_X_X XXXXXX X_XXX XXXXXX X_XX_X X_XX_X X_X_X XX_XXX
 RESULT : TWO MATCHES FOR A PREDETERMINED SECOND POSITION AND FOURTH
 POSITION → SCANNING INVALID ; POSITION CANNOT BE DETERMINED

FIG. 5f

POSITION : 111010 110100 101000 010001 100011 000111 001110 011101
 SCANNING : 110000 110000 110000 110000 110000 110000 110000 110000
 COMPARISON: XX_X_X XXX_XX X_XXX _XXXX_ X_XX_ _X_ _X_ X_X_X_
 RESULT : NO MATCHES → ERRONEOUS BITS ARE INCLUDED THAT WERE NOT MARKED